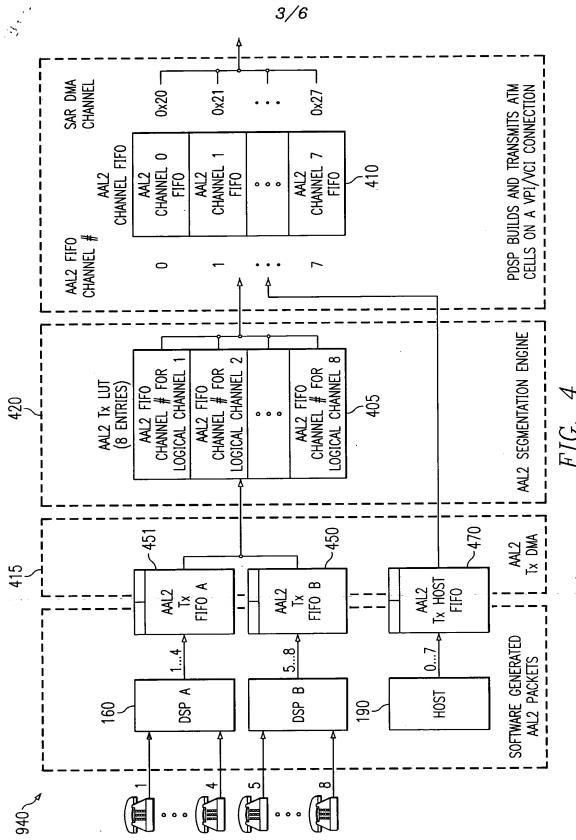
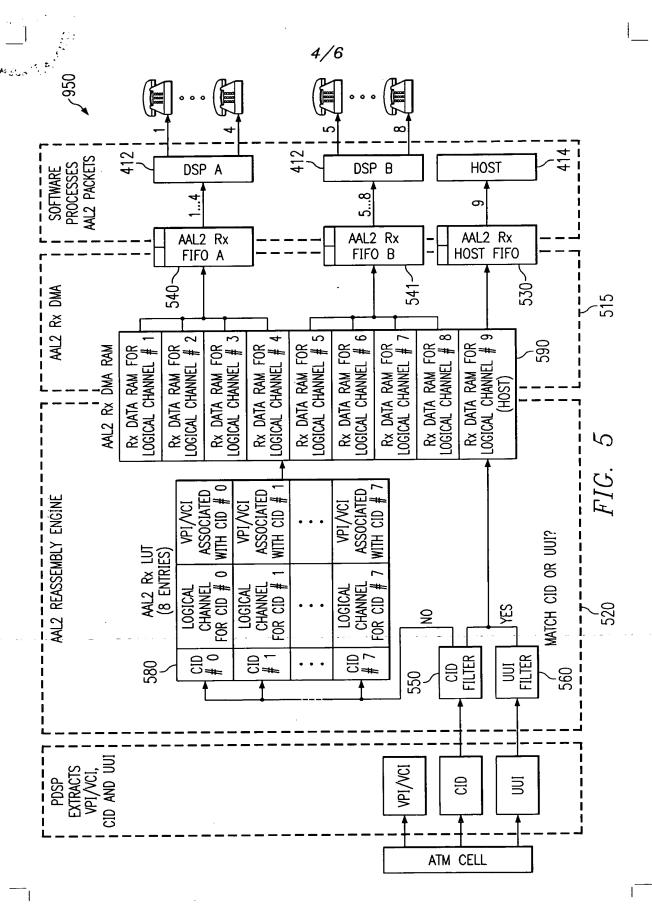
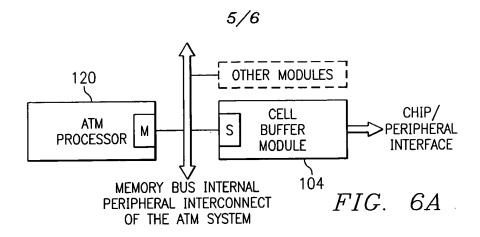
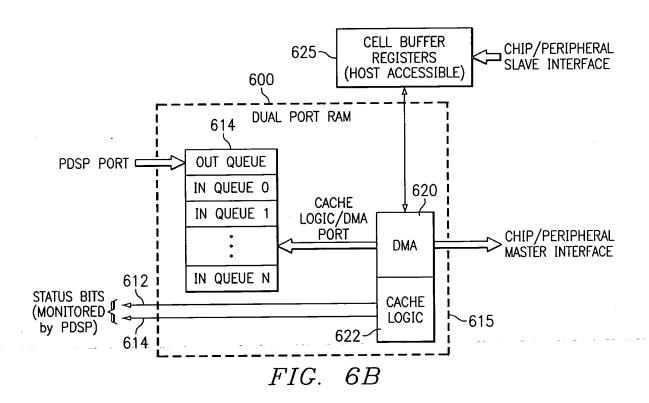


FIG. 3









56-BYTES ALIGNED ATM CELL

ATM HEADER (5 BYTES)	
ATM PAYLOAD (BYTE 1 THRU 47)	
BYTE 48	PORT ID, PRIORITY LEVEL

FIG. 7

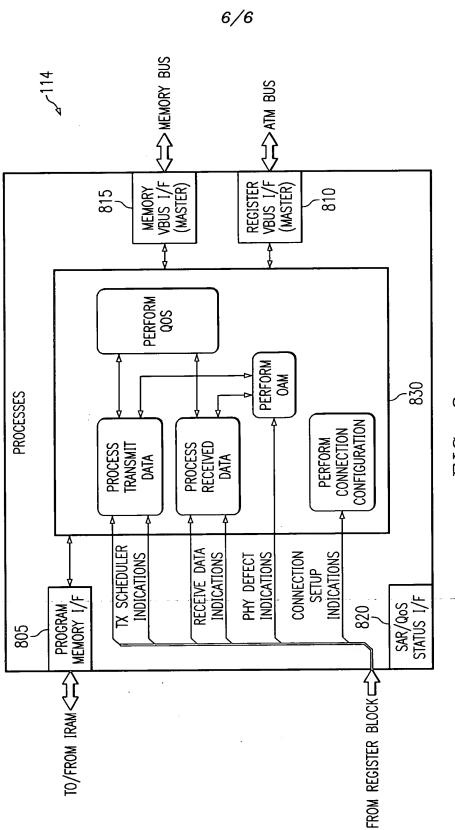


FIG. 8